

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	14	714/14.ccls. and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 16:39
L2	336	713/300.ccls. and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 14:13
L3	325	713/300.ccls. and @ad>="20011015" and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 13:59
L4	11	713/300.ccls. and @ad<="20011015" and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 14:00
L5	70	713/324.ccls. and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 14:28
L6	60	713/340.ccls. and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 14:41
L7	101	714/4.ccls. and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 14:48
L8	38	714/11.ccls. and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:06
L9	10	714/18.ccls. and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:07

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L10	15	714/22.ccls. and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:09
L11	52	714/43.ccls. and @pd>="20061207"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:09
L12	638	"split path"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:32
L13	7	"split path" and (redundant with power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 16:41
L14	31	(WILLIAMS-RICK WILLIAMS-RICKI-D WILLIAMS-RICKI-DEE GILBERT-GARY GILBERT-GARY-L). in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:35
L15	4	("5361249" "6067286").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:39
L16	2	EP-543582-\$.did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:39
L17	10	("6757243" "6748429").pn. ("20030061534" "20030061476" "20010056553").did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:40
L18	8507	(redundant with power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:58
L19	2495	(redundant adj power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:58

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L20	42	(redundant adj power) near4 (first and second)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:59
L21	7811	(dual or redundant or split) adj (path or bus or \$4plane)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 16:21
L22	264	L21 and (redundant near2 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 16:38
L23	14	L21 and ((redundant near2 power) with (first and second))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 16:22
S1	25	(WILLIAMS-RICK WILLIAMS-RICKI-D WILLIAMS-RICKI-DEE GILBERT-GARY GILBERT-GARY-L). in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 15:35
S2	200	714/14.ccls. and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/06 14:14
S3	1034	713/300.ccls. and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/06 15:55
S4	493	713/324.ccls. and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/11 15:09
S5	474	713/340.ccls. and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/13 08:42
S7	36	(S3 or S4 or S5) and ((redundant or dual) adj ("power supply"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 08:44

EAST Search History

S8	622	dual adj "power supply"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/03 08:54
S9	496	S8 and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/03 10:04
S10	14	S9 and ("split path" or backplane or midplane)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/03 09:17
S11	0	S9 and ("split path")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 14:06
S12	10	("6757243" "6748429").pn. ("20030061534" "20030061476" "20010056553").did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 10:30
S13	2	714/14.ccls. and @ad<="20011015" and @pd>="20041203"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 12:50
S14	2	"5977650".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 14:05
S15	489	("split path")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 08:48
S16	258	S15 and parallel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 14:18
S17	33	S16 and redundant	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/27 14:18

EAST Search History

S18	21	S17 and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 08:48
S19	1070	713/300.ccls. and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 08:44
S20	509	713/324.ccls. and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 09:05
S21	494	713/340.ccls. and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 08:44
S22	2	(S19 or S20 or S21) and ((redundant or dual) adj ("power supply")) and @pd>="20041203"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 08:44
S23	18	("split path") with parallel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 08:48
S24	13	S23 and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 08:48
S25	1237	parallel with backplane	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 09:23
S26	17	S25 and "redundant power supply"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 09:23
S27	13	S26 and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/28 09:23

EAST Search History

S28	5	714/14.ccls. and @ad<="20011015" and @pd>="20050428"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 14:13
S29	40	713/300.ccls. and @ad<="20011015" and @pd>="20050428"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 14:14
S30	20	713/324.ccls. and @ad<="20011015" and @pd>="20050428"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 14:18
S31	19	713/340.ccls. and @ad<="20011015" and @pd>="20050428"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 14:34
S32	516	("split path")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 09:19
S33	3	S32 and (redundant near2 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/13 09:27
S34	1314	parallel with backplane	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/13 10:58
S35	18	S34 and "redundant power supply"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/13 10:55
S36	14	S35 and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/13 10:58
S37	187	parallel adj backplane	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 12:51

EAST Search History

S38	139	S37 and @ad<="20011015"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/13 10:58
S39	39	S38 and "power supply"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/13 10:58
S40	17	dual adj backplane	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/13 13:26
S41	2	714/14.ccls. and @ad<="20011015" and @pd>="20051013"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 08:44
S42	26	713/300.ccls. and @ad<="20011015" and @pd>="20051013"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 08:45
S43	17	713/324.ccls. and @ad<="20011015" and @pd>="20051013"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 09:05
S44	10	713/340.ccls. and @ad<="20011015" and @pd>="20051013"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 09:06
S45	4	("5361249" "6067286").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 12:29
S46	0	EP-0543582-\$.did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 12:43
S47	2	EP-543582-\$.did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 12:48

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S48	8	"795215".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 12:48
S49	225	(dual or parallel) adj backplane	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 12:58
S50	0	714/14.ccls. and @ad<="20011015" and @pd>="20060331"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 13:57
S51	15	713/300.ccls. and @ad<="20011015" and @pd>="20060331"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 09:00
S52	7	713/324.ccls. and @ad<="20011015" and @pd>="20060331"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 13:44
S53	7	713/340.ccls. and @ad<="20011015" and @pd>="20060331"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 13:49
S54	559	("split path")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 09:19
S55	116	S54 and ((redundant or dual or two or backup) with (power))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 09:19
S56	0	714/14.ccls. and @ad<="20011015" and @pd>="20060621"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/06 12:29
S57	25	713/300.ccls. and @ad<="20011015" and @pd>="20060621"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 09:01

EAST Search History

S58	14	713/324.ccls. and @ad<="20011015" and @pd>="20060621"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 13:44
S59	10	713/340.ccls. and @ad<="20011015" and @pd>="20060621"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 13:49
S60	276	714/14.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/06 13:04
S61	1693	714/4.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/06 12:33
S62	0	714/4.ccls. and "split path"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/06 13:04
S63	21	714/4.ccls. and (redundant adj2 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/06 13:04
S64	668	714/11.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/06 13:04
S65	0	714/11.ccls. and "split path"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/06 14:38
S66	15	714/11.ccls. and (redundant adj2 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/06 14:38
S67	196	714/18.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 09:21

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S68	0	714/18.ccls. and "split path"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/08 08:40
S69	0	714/18.ccls. and (redundant adj2 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 09:24
S70	3	714/18.ccls. and "redundant path"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 09:22
S71	314	714/22.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 15:44
S72	0	714/22.ccls. and "split path"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 16:03
S73	3	714/22.ccls. and "redundant path"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 15:44
S74	22	714/22.ccls. and (redundant adj2 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 16:03
S75	812	714/43.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 15:44
S76	21	714/43.ccls. and "redundant path"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 15:44
S77	0	714/43.ccls. and "split path"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 16:03

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S78	2	714/43.ccls. and (redundant adj2 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 16:03
S79	602	"split path"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/08 08:40
S80	3	"split path" and (redundant adj2 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/08 09:04
S81	34044	message near2 (part portion half section)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/08 09:05
S82	816	message near2 (part portion half section) near2 (first and second)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/08 09:06
S83	50	S82 and (power adj supply)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/08 09:07
S84	0	S82 and (redundant adj power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/08 09:07
S85	18	"PCI express" and (redundant adj power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 12:32

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L21	7811	(dual or redundant or split) adj (path or bus or \$4plane)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/31 16:21
L24	127	L21 and (redundant near2 power)	US-PGPUB	OR	ON	2007/05/31 16:38
L26	45	714/14.ccls.	US-PGPUB	OR	ON	2007/05/31 16:39
L27	2	"split path" and (redundant with power)	US-PGPUB	OR	ON	2007/05/31 16:41

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Method and apparatus for supplying redundant power - Patent ...

The apparatus of claim 1, further comprising a third and a fourth **power supply** adapted to provide **redundant power** to a second **split path** through the ...

www.freepatentsonline.com/20030074586.html - 55k -

[Cached](#) - [Similar pages](#)

Power Supply Redundant

AC/DC & DC/DC. Ships within 3 days.
 1v to 30kV voltages. Made in USA.
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Method and apparatus for supplying redundant power - Patent EP1302839

providing a **redundant** source of **power** to a **split path**, wherein the **split path** is For example, in one embodiment, an out-of-spec **redundant power supply** ...

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[PDF] 2U Rack redundant power supply

File Format: PDF/Adobe Acrobat - [View as HTML](#)

This 2U Rack **Power Supply** Design Guide defines a common **redundant power** of the pins of the **power** assembly wire harness provides the **power return path**. ...

ssiforum.org/Power%20Supplies/ERP2U%20Spec%202_31.pdf - [Similar pages](#)

[PDF] BROCADE AP7420

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Features a high-performance **split-path** ... failover-capable Data Path Controllers

Redundant power supply, SFP media. BRoCADE AP7420 sPECiFiCatioNs ...

www.brocade.com/san/pdf/datasheets/BrocadeAP7420_DS_00.pdf - [Similar pages](#)

[PPT] Slide 1

File Format: Microsoft Powerpoint - [View as HTML](#)

Split path (SPAID) architecture. Optimizes throughput, highly scalable; 460000 IOPs.

Enterprise class HA. Multi-**path** failover, clustering; **Redundant power** ...

www.qlogic.com/news-events/webcast_files/QLogic_Virtual_Platform.ppt - [Similar pages](#)

SGI TPL (Hardware: Administrative/RAID_OG - Chapter 2. Storage ...

If **redundant power supply** module is present, applications continue running; ... The **split**-bus configuration has two hosts, each with a SCSI-2 interface ...

techpubs.sgi.com/library/tpl/cgi-bin/getdoc.cgi/

hdwr/bks/SGI_Admin/books/RAID_OG/sgi_html/ch02.html - 35k - [Cached](#) - [Similar pages](#)

Multiple-path interface card for interfacing multiple isolated ...

Each BCC can be operated in a **split-bus** configuration with only a single bus If a blower fails, the **power supply** 1300 shuts down. Optional **redundant** ...

www.patentstorm.us/patents/6748477-description.html - 43k - [Cached](#) - [Similar pages](#)

[PDF] Data Sheet - Model 6940 Node 870MHz with 65/86 split

File Format: PDF/Adobe Acrobat

powering port, optional **redundant power supply**, and extended 6940/44 – Reverse Switch (one may be ordered for each reverse input port or common path) ...www.sciatl.com/customers/Source/744481.pdf - [Similar pages](#)**[PDF] The SG2000 Optical Node sets the industry standard for performance ...**File Format: PDF/Adobe Acrobat - [View as HTML](#)**split-band** or **redundant** functionality. In **split-band** applications, the ... Each DC **power supply** can deliver the total **power** ...broadband.motorola.com/catalog/product_documents/SG2000_509968-001.pdf -[Similar pages](#)**[PDF] Data Sheet - Model GS7000 4-Port Node with 65/86 MHz Split**

File Format: PDF/Adobe Acrobat

Primary and **redundant Power** Supplies with passive load sharing typical CATV type ferro-resonant AC **power supply** (quasi-square wave). ...www.scientificatlanta.com/customers/Source/7011572.pdf - [Similar pages](#)[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) **[Next](#)**Try [Google Desktop](#): search your computer as easily as you search the web.

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Relevance scale ☐ ☐ ☐ ☐ ☐**1 [The relational model for database management: version 2](#)**

E. F. Codd

January 1990 Book

Publisher: Addison-Wesley Longman Publishing Co., Inc.Full text available: pdf(28.61 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)**From the Preface (See Front Matter for full Preface)**

An important adjunct to precision is a sound theoretical foundation. The relational model is solidly based on two parts of mathematics: firstorder predicate logic and the theory of relations. This book, however, does not dwell on the theoretical foundations, but rather on all the features of the relational model that I now perceive as important for database users, and therefore for DBMS vendors. My perceptions result from 20 y ...

2 [System-level power optimization: techniques and tools](#)

Luca Benini, Giovanni de Micheli

April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**,
Volume 5 Issue 2**Publisher:** ACM PressFull text available: pdf(385.22 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic systems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, communication, and storage units, and we review methods of reducing their energy consumption. We also study models for analyzing the energy cost of software, and methods for energy-efficient software design and compilation. This survey ...

3 [Power reduction techniques for microprocessor systems](#)

Vasanth Venkatachalam, Michael Franz

September 2005 **ACM Computing Surveys (CSUR)**, Volume 37 Issue 3**Publisher:** ACM PressFull text available: pdf(602.33 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Power consumption is a major factor that limits the performance of computers. We survey the "state of the art" in techniques that reduce the total power consumed by a microprocessor system over time. These techniques are applied at various levels ranging

from circuits to architectures, architectures to system software, and system software to applications. They also include holistic approaches that will become more important over the next decade. We conclude that power management is a ...

Keywords: Energy dissipation, power reduction

4 Embedded tutorial 2: Low power light-weight embedded systems



Majid Sarrafzadeh, Foad Dabiri, Roozbeh Jafari, Tammara Massey, Ani Nahapetan

October 2006 **Proceedings of the 2006 international symposium on Low power electronics and design ISLPED '06**

Publisher: ACM Press

Full text available: pdf(453.38 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Light-weight embedded systems are now gaining more popularity due to the recent technological advances in fabrication that have resulted in more powerful tiny processors with greater communication capabilities that pose various scientific challenges for researchers. Perhaps the most significant challenge is the **energy consumption concern and reliability**, mainly due to the small size of batteries. In this tutorial, we portray a brief description of low-power, light-weight embedded systems, ...

Keywords: light-weight embedded systems, power optimization, sensor networks

5 Simplify: a theorem prover for program checking



David Detlefs, Greg Nelson, James B. Saxe

May 2005 **Journal of the ACM (JACM)**, Volume 52 Issue 3

Publisher: ACM Press

Full text available: pdf(1.93 MB) Additional Information: [full citation](#), [appendices and supplements](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

This article provides a detailed description of the automatic theorem prover Simplify, which is the proof engine of the Extended Static Checkers ESC/Java and ESC/Modula-3. Simplify uses the Nelson--Oppen method to combine decision procedures for several important theories, and also employs a matcher to reason about quantifiers. Instead of conventional matching in a term DAG, Simplify matches up to equivalence in an E-graph, which detects many relevant pattern instances that would be missed by th ...

Keywords: Theorem proving, decision procedures, program checking

6 Compiler construction: an advanced course

F. L. Bauer, F. L. De Remer, M. Griffiths, U. Hill, J. J. Horning, C. H. A. Koster, W. M. McKeeman, P. C. Poole, W. M. Waite, G. Goos, J. Hartmanis
January 1974 Book

Publisher: Springer-Verlag New York, Inc.


Full text available: pdf(65.62 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

The Advanced Course took place from March 4 to 15, 1974 and was organized by the Mathematical Institute of the Technical University of Munich and the Leibniz Computing Center of the Bavarian Academy of Sciences, in co-operation with the European Communities, sponsored by the Ministry for Research and Technology of the Federal Republic of Germany and by the European Research Office, London.

Selected writings on computing: a personal perspective

Edsger W. Dijkstra
January 1982 Book

Publisher: Springer-Verlag New York, Inc.

Full text available:  pdf(60.98 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)


Since the summer of 1973, when I became a Burroughs Research Fellow, my life has been very different from what it had been before. The daily routine changed: instead of going to the University each day, where I used to spend most of my time in the company of others, I now went there only one day a week and was most of the time that is, when not travelling!-- alone in my study. In my solitude, mail and the written word in general became more and more important. The circumstance that my employe ...

8 DiCER: distributed and cost-effective redundancy for variation tolerance

Di Wu, G. Venkataraman, Jiang Hu, Quiyang Li, R. Mahapatra

May 2005 **Proceedings of the 2005 IEEE/ACM International conference on Computer-aided design ICCAD '05**

Publisher: IEEE Computer Society

Full text available:  pdf(237.57 KB) Additional Information: [full citation](#), [abstract](#)

Increasingly prominent variational effects impose imminent threat to the progress of VLSI technology. This work explores redundancy, which is a well-known fault tolerance technique, for variation tolerance. It is observed that delay variability can be reduced by making redundant paths distributed or less correlated. Based on this observation, a gate splitting methodology is proposed for achieving distributed redundancy. We show how to avoid short circuit and estimate delay in dual-driver nets wh ...

9 Design techniques for high performance, energy efficient control logic


Uming Ko, Anthony Hill, Poras T. Balsara

August 1996 **Proceedings of the 1996 international symposium on Low power electronics and design ISLPED '96**

Publisher: IEEE Press

Full text available:  pdf(102.90 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

10 GPGPU: general purpose computation on graphics hardware

 David Luebke, Mark Harris, Jens Krüger, Tim Purcell, Naga Govindaraju, Ian Buck, Cliff Woolley, Aaron Lefohn

August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**

Publisher: ACM Press

Full text available:  pdf(63.03 MB) Additional Information: [full citation](#), [abstract](#), [citations](#)

The graphics processor (GPU) on today's commodity video cards has evolved into an extremely powerful and flexible processor. The latest graphics architectures provide tremendous memory bandwidth and computational horsepower, with fully programmable vertex and pixel processing units that support vector operations up to full IEEE floating point precision. High level languages have emerged for graphics hardware, making this computational power accessible. Architecturally, GPUs are highly parallel s ...

11 Classics in software engineering

January 1979 Divisible Book

Publisher: Yourdon Press

Full text available:  pdf(22.45 MB) Additional Information: [full citation](#), [cited by](#), [index terms](#)

12 Essays in computing science


C. A. R. Hoare
January 1989 Book

Publisher: Prentice-Hall, Inc.

Full text available:  pdf(20.91 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [review](#)


Charles Antony Richard Hoare is one of the most productive and prolific computer scientists. This volume contains a selection of his published papers. There is a need, as in a Shakespearian Chorus, to offer some apology for what the book manifestly fails to achieve. It is not a complete 'collected works'. Selection between papers of this quality is not easy and, given the book's already considerable size, some difficult decisions as to what to omit have had to be made. Pity the editor weighin ...

13 Work-preserving emulations of fixed-connection networks

 Richard R. Koch, F. T. Leighton, Bruce M. Maggs, Satish B. Rao, Arnold L. Rosenberg, Eric J. Schwabe

January 1997 **Journal of the ACM (JACM)**, Volume 44 Issue 1

Publisher: ACM Press

Full text available:  pdf(719.89 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#), [review](#)

Keywords: graph embeddings, network emulations, parallel architectures, processor arrays

14 Artificial intelligence

Elaine Rich
January 1983 Book

Publisher: McGraw-Hill, Inc.

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [review](#)

The goal of this book is to provide programmers and computer scientists with a readable introduction to the problems and techniques of artificial intelligence (A.I.). The book can be used either as a text for a course on A.I. or as a self-study guide for computer professionals who want to learn what A.I. is all about.

The book was designed as the text for a one-semester, introductory graduate course in A.I. In such a course, it should be possible to cover all of the material in the boo ...

15 Anatomy of LISP

John Allen
January 1978 Book

Publisher: McGraw-Hill, Inc.

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

This text is nominally about LISP and data structures. However, in the process it covers much broader areas of computer science. The author has long felt that the beginning student of computer science has been getting 'a distorted and disjointed picture of the field. In some ways this confusion is natural; the field has been growing at such a rapid rate that few are prepared to be judged experts in all areas of the discipline. The current alternative seems to be to give a few introductory cou ...

16 Routing optimizations: Minimum energy disjoint path routing in wireless ad-hoc

**networks**

Anand Srinivas, Eytan Modiano

September 2003 **Proceedings of the 9th annual international conference on Mobile computing and networking MobiCom '03****Publisher:** ACM PressFull text available: [pdf\(452.89 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We develop algorithms for finding minimum energy disjoint paths in an all-wireless network, for both the node and link-disjoint cases. Our major results include a novel polynomial time algorithm that optimally solves the minimum energy 2 link-disjoint paths problem, as well as a polynomial time algorithm for the minimum energy k node-disjoint paths problem. In addition, we present efficient heuristic algorithms for both problems. Our results show that link-disjoint paths consume substantially le ...

Keywords: disjoint paths, distributed algorithms, energy efficiency, minimum energy, multipath routing, wireless ad-hoc networks

17 Finding minimum energy disjoint paths in wireless ad-hoc networks

Anand Srinivas, Eytan Modiano

July 2005 **Wireless Networks**, Volume 11 Issue 4**Publisher:** Kluwer Academic PublishersFull text available: [pdf\(2.33 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We develop algorithms for finding minimum energy disjoint paths in an all-wireless network, for both the node and link-disjoint cases. Our major results include a novel polynomial time algorithm that optimally solves the minimum energy 2 link-disjoint paths problem, as well as a polynomial time algorithm for the minimum energy k node-disjoint paths problem. In addition, we present efficient heuristic algorithms for both problems. Our results show that link-disjoint paths consume substanti ...

Keywords: disjoint paths, distributed algorithms, energy efficiency, graph theory, multipath routing, wireless ad-hoc networks

18 Real-time shading

Marc Olano, Kurt Akeley, John C. Hart, Wolfgang Heidrich, Michael McCool, Jason L. Mitchell, Randi Rost

August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04****Publisher:** ACM PressFull text available: [pdf\(7.39 MB\)](#) Additional Information: [full citation](#), [abstract](#)

Real-time procedural shading was once seen as a distant dream. When the first version of this course was offered four years ago, real-time shading was possible, but only with one-of-a-kind hardware or by combining the effects of tens to hundreds of rendering passes. Today, almost every new computer comes with graphics hardware capable of interactively executing shaders of thousands to tens of thousands of instructions. This course has been redesigned to address today's real-time shading capabili ...

19 A survey of research and practices of Network-on-chip

Tobias Bjerregaard, Shankar Mahadevan

June 2006 **ACM Computing Surveys (CSUR)**, Volume 38 Issue 1**Publisher:** ACM PressFull text available: [pdf\(1.41 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The scaling of microchip technologies has enabled large scale systems-on-chip (SoC).

Network-on-chip (NoC) research addresses global communication in SoC, involving (i) a move from computation-centric to communication-centric design and (ii) the implementation of scalable communication structures. This survey presents a perspective on existing NoC research. We define the following abstractions: system, network adapter, network, and link to explain and structure the fundamental concepts. First, r ...

Keywords: Chip-area networks, GALS, GSI design, NoC, OCP, SoC, ULSI design, communication abstractions, communication-centric design, interconnects, network-on-chip, on-chip communication, sockets, system-on-chip

20 Semi-custom techniques in system design: Priority assignment optimization for minimization of current surge in high performance power efficient clock-gated microprocessor

Yiran Chen, Kaushik Roy, Cheng-Kok Koh

January 2004 **Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair ASP-DAC '04 , Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair ASP-DAC '04**

Publisher: IEEE Press

Full text available:  pdf(272.86 KB)

Additional Information: [full citation](#), [abstract](#), [references](#)



[Publisher Site](#)

We propose an integrated architectural/physical-planning approach named priority assignment optimization to minimize the current surge in high performance power efficient clock-gated microprocessors. The proposed approach balances the current demands across the floorplan by assigning optimized priorities to the functional units (FUs). Two complementary methods -- physical planning with soft modules and issue pattern management - to enhance our proposed approach are also discussed for various app ...

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